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March 19, 2009

FACSIMILE TRANSMITTAL

FROM: Peter J. Phillips
DATE: March 19, 2009
RE: Application No. 10/800,213
Telephone Interview Agenda
Friday, March 20, 2009, 11:00 AM

TO: M. Riley
FAX: (571) 273-8300

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Our Ref: SUZ-31

Client Ref.: SO-1539KKUS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: M. Riley
Re: Application of: K. OKUYAMA
Application No. 10/800,213
Filed: March 12, 2004
For: PRINT DATA PROCESSING APPARATUS AND
DATA PROCESSING APPARATUS
Art Unit: 2625
Confirmation No.: 8568

March 19, 2009

Via Facsimile: (571) 273-8300

TELEPHONE INTERVIEW AGENDA

Applicant notes with appreciation the telephone interview schedule for Friday, March 20, 2009, at 11:00 A.M.

The Examiner, Marcus Riley, requested an Agenda for the interview. Accordingly, applicant has prepared this Agenda listing points applicant wishes to cover:

1. Whether Shima USP 6,104,498 (in Fig. 11, column 13, lines 6 – 17, or otherwise) discloses resuming the writing in a receiving buffer (and stopping of the writing to an auxiliary buffer) when free space in the receiving has reached a pre-determined value.
2. Whether Brown USP 6,046,817 discloses shifting of writing to a receiving buffer to an auxiliary buffer, and then back to the receiving buffer during the process of

receiving data.


3. Whether Emote USP 6,788,430 discloses shifting of writing to a receiving buffer to an auxiliary buffer, and then back to the receiving buffer during the process of receiving data.
4. Whether any of the above three references discloses a receiving controller which temporarily stops receiving process of the receiving buffer when the receiving buffer is full, and cancels the temporary stoppage and re-starts the receiving process of the receiving buffer when the free space in the receiving buffer exceeds a pre-determined value.
5. Whether any of the above three references discloses a write controller which starts to transfer data from the receiving buffer into an auxiliary buffer when the free space in the receiving buffer has run out, and stops the writing process when the free space in the receiving buffer is above a pre-determined value.
6. Whether any of the above three references discloses a process of transferring data from a receiving buffer to an auxiliary storage device, and a write controller which destroys data which was previously written into the auxiliary storage device, while simultaneously emptying the space in the receiving buffer during the writing process.

7. Whether any additional features can be added to the independent Claims 21 and 27, which would overcome the Examiner's rejections.

Respectfully submitted

LUCAS & MERCANTI, LLP

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CERTIFICATE OF TRANSMISSION

I hereby certify that this document is being transmitted to the Commissioner for Patents via Facsimile to (571) 273-8300 on March 19, 2009.

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